



For Output K	Allocation Status (B = 0, 1, 2)	Input Controller (I = 1 to 7)	Waiting Input Controllers (W)	Credits (C)	Flits Present (P)
			7 _ _ _ _ _ _ _		
1					
2					
3					
•					
•					
•					
V					

State
vector
s[V,K]

FIG. 12



FIG. 13